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02/18/2004

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. P.O. BOX 2938 MINNEAPOLIS, MN 55402

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| MAY 18, 2004     | (Date)             |
|                  |                    |

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/551,027      | 04/17/2000  | Wendell P. Noble     | 303.379US2          | 1158             |

TITLE OF INVENTION: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

| APPLN. TYPE                              | SMALL ENTITY  | ISSUE F            | EE  | PUBLICATION FEE  | TOTAL FEE(S) DUE  | DATE DUE                               |  |
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| nonprovisional                           | NO  | \$1330             | 0   | \$300  | \$1630  | 05/18/2004                             |  |
| EXAMINER ART UN TRINH, MICHAEL MANH 2822 |   | IIT                | CLASS-SUBCLASS                                  | 7  |   |  |  |
|  |   | 2822               |   | 438-243000   | _   |  |  |
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Wendell P. Noble Jr. et al.

Title: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

Docket No.: 303.379US2 Filed: April 17, 2000 Examiner: Michael Trinh

Customer No.: 21186

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Serial No.: 09/551,027

Due Date: May 18, 2004

Group Art Unit: 2822

Confirmation No.: 1158

**Notice of Allowance Date:** 

February 18, 2004

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